DOCKET NO.: **BUR920000098US1**

In re Application of:	§	
J. SMITH ET AL.	§	Examiner: **T. VO**
	§	
Serial No.: **09/681,327**	§	Art Unit: **2122**
	§	
Filed: **MARCH 19, 2001**	§	
	§	
For: **METHOD AND SYSTEM FOR**	§	
OPTIMIZING CODE USING AN	§	
OPTIMIZING COPROCESSOR	§	

LETTER TO THE OFFICIAL DRAFTSMAN

DRAWING REVIEW BRANCH
U. S. Patent and Trademark Office
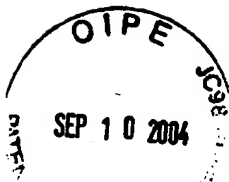Alexandria, Virginia 22313-1450

Sir:

Submitted herewith are four (4) sheets of duplicate/replacement formal drawings, pursuant to the Notice of Allowability for the above-identified application, including a copy of the return postcard indicating previous receipt of these formal drawings in June 2001. The original formal drawings were accepted by the Examiner in his Office Action of August 14, 2000.

Respectfully submitted,

Andrew J. Dillon
Registration No. 29,634
DILLON & YUDELL L.L.P.
P.O. Box 201720
Austin, Texas 78720-1720
(512) 343.6116

7/26/0ℓ
(Date)

ATTORNEY FOR APPLICANT(S)

Received in the U.S.P.T.O
In re Application of
JACK ROBERT SMITH, ET AL.
METHOD AND SYSTEM FOR OPTIMIZING CODE USING
AN OPTIMIZING COPROCESSOR
ENCLOSED HEREWITH:
1.   Letter to the Official Draftsman;
2.   Four (4) sheets of formal drawings;
3.   Certificate of Mailing; and
4.   Our return postcard
IBM CORPORATION
Serial No. 09/681,327; Filed: March 19, 2001
Our File No. 0245AD-BUR20000098US1
Mailed: **June 5, 2001**
APN/dm